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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10069592	FILING DATE 07/16/2002	CLASS 174	SUBCLASS 110R	GAU 2831	EXAMINER MAYO
<b>**APPLICANTS:</b> Ajiki Kenichi;					
<b>**CONTINUING DATA VERIFIED:</b> This application is a 371 of PCT/JP01/05628 06/29/2001					
<b>BEST AVAILABLE COPY</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2000-196088 06/29/2000					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 2002_0229A	
Verified and Acknowledged Examiners's initials					
TITLE : Covered conductor, soldering method using the covered conductor, and electric acoustic converter					

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner		<b>CLAIMS ALLOWED</b>	
				Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner		<b>DRAWING</b>	
Amount Due	Date Paid			Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>		Applicati n Examiner	
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